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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent Application Serial No. 10/735,355
Filing Date December 12, 2003
Inventor Zhongze Wang
Assignee Micron Technology, Inc.
Group Art Unit Unassigned
Examiner Unknown
Attorney Docket No. MI22-2457
Title: Wafer Bonding Method of Forming Silicon-on-Insulator Comprising
Integrated Circuitry

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

CERTIFICATE OF FACSIMILE TRANSMISSION UNDER 37 CFR § 1.8

I hereby certify that the following papers are being facsimile transmitted to the Patent and Trademark Office at (703) 872-9306 on the date shown below:

1. Certificate of Facsimile Transmission
2. Supplemental Information Disclosure Statement
3. Form PTO-1449 w/listed cited reference

Dated: Dec. 29, 2003

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Although it is believed that no fees are due, the Commissioner is hereby authorized to charge any fees under 37 C.F.R. 1.16 and 1.17 which may be required by this paper to Deposit Account No. 23-0925.

Dated: 12-29-03

By: Mark S. Matkin
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SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

References -See Attached Form PTO-1449

The Examiner's attention is directed to the reference which is listed on the attached Form PTO-1449. No admission is made regarding whether the reference is prior art.

Citation of the referenced art is respectfully requested.

This Supplemental Information Disclosure Statement is being filed before the mailing date of a first Office Action. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this Supplemental Information Disclosure Statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Respectfully submitted,

Dated: 12-29-03

By: 

Mark S. Matkin
Reg. No. 32,268

| Form PTO-1449 | | U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE | | | ATTY. DOCKET NO. M122-2457 | | SERIAL NO. 10/735,355 | |
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| LIST OF ART CITED BY APPLICANT (Use several sheets if necessary) | | | | | APPLICANT: Zhongze Wang | | | |
| | | | | | FILING DATE December 12, 2003 | | GROUP ART UNIT Unassigned | |
| U.S. PATENT DOCUMENTS | | | | | | | | |
| Examiner's Initials | Document Number | Date | Name | Class | Subclass | Filing Date if Appropriate | | |
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